

Abstract of the DisclosureAN INSTRUCTION SET FOR A COMPUTER

An instruction set for a computer is described which includes instructions having a common predetermined bit length. That predetermined bit length can define a single operation or two independent operations. The instruction includes designated bits at predetermined bit locations which identify whether the instruction is a long instruction or a dual operation instruction.

00000000 00000000 00000000 00000000 00000000 00000000 00000000 00000000